



PATENT  
Conf. No.: 6712

IN THE UNITED STATES PATENT OFFICE

Applicants: Jay T. Young et al.

Assignee: Xilinx, Inc.

Title: "Methods of Generating Test Designs for Testing Specific Routing Resources in Programmable Logic Devices"

Serial No.: 10/696,357

File Date: 10/28/2003

Examiner: Sun J. Lin

Art Unit: 2825

Docket No.: X-1434 US

Conf. No.: 6712

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MAIL STOP AMENDMENT  
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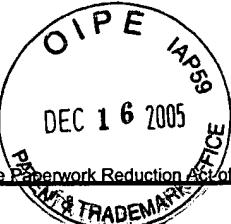
AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on September 21, 2005, please reconsider allowance of the claims in view of the Amendments and Remarks below.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 10 of this paper.



PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031  
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JFW

## TRANSMITTAL FORM

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Total Number of Pages in This Submission

Application Number	10/696,357
Filing Date	October 28, 2003
First Named Inventor	Jay T. Young
Art Unit	2825
Examiner Name	Sun J. Lin

Attorney Docket Number X-1434 US

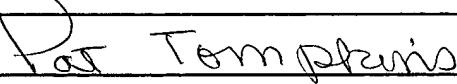
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Remarks		

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	XILINX, INC. CUSTOMER NO: 24309		
Signature			
Printed name	Lois D. Cartier		
Date	December 13, 2005	Reg. No.	40,941

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Signature			
Typed or printed name	Pat Tompkins	Date	December 13, 2005

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